

In the Specification

Please delete the paragraph beginning on page 1, after "Cross-Reference to Related Applications" and replace it with the following paragraph:

The present application is related to commonly assigned U.S. patent application Serial Patent No. 7,010,776, _____ (Attorney Docket No. FIS920030107, titled "EXTENDING THE RANGE OF LITHOGRAPHIC SIMULATION INTEGRALS"), U.S. patent application Serial No. _____ (Attorney Docket No. FIS920030108/10/694,465, titled "INCORPORATION OF A PHASE MAP INTO FAST MODEL-BASED OPTICAL PROXIMITY CORRECTION SIMULATION KERNELS TO ACCOUNT FOR NEAR AND MID-RANGE FLARE"), U.S. patent application Serial Patent No. 7,055,126, _____ (Attorney Docket No. FIS920030110, titled "RENESTING INTERACTION MAP FLAREMAP INTO DESIGN FOR EFFICIENT LONG RANGE ~~LONG RANGE~~ FLARE CALCULATION"), and U.S. patent application Serial No. _____ (Attorney Docket No. FIS920030262/10/694,299, titled "SIMULTANEOUS ~~PARALLEL~~ COMPUTATION OF MULTIPLE POINTS ON ONE OR MULTIPLE CUT LINES"), filed on even date herewith, which are hereby incorporated by reference herein in their entirety.

Please delete the paragraph on page 21, line 28 to page 22, line 6, and replace it with the following paragraph:

In all aspects of the invention, the present pinning method and algorithm for use in OPC also facilitates the use of triangle convolution as disclosed in ~~co-pending U.S. patent~~

application Serial No. _____ (~~Attorney Docket No. FIS920030107~~ U.S. Patent No. 7,010,776), titled "EXTENDING THE RANGE OF LITHOGRAPHIC SIMULATION INTEGRALS"), which does not require that the kernel be spatially truncated, but rather, that the mask be truncated into a square area with a size of $(2x \text{ ROI})^2$. That is, the table memory size in the present invention may be advantageously reduced to within $(2x \text{ ROI})^2$, even for 45-degree sectors (a benefit of eliminating skewed tables).